1.S. PTO 76197 1	565	Subclass	SSUE CLASSIFICATION
10913 U	138	Class	SSUE CLA

PATENT NUMBER

U.S. UTILITY Patent Application

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, O.I.P.E.	PATENT DATE				
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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
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William Roche David Wu Massud Aminpur Scott Luning

ABANDONEL

Method of forming a gate electrode on a semiconductor device and a device incorporating same $\dot{}$

		ISSUING	CLASSI	FICATION	ON			
ORIGINA	AL.	CROSS REFERENCE(S)						
CLASS	SUBCLASS	CLASS SUBCLASS (ONE SUBCLASS PER BLOCK)						
438	595	1438	197	585	669	659	7/4	
INTERNATIONAL	CLASSIFICATION	1						· ·
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TERMINAL	DRAWINGS			CLAIMS ALLOWED			
☐ DISCLAIMER	Sheets Drwg. Figs, Drwg.		Print Fig.	Total Claims	Print Claim for O.G.		
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☐ The term of this patent	KHANH B. DUONG			NOTICE OF ALLOWANCE MAILED			
subsequent to (date) has been disclaimed.	(Assistant Examiner) / (Date)		12-3-02				
The term of this patent shall not extend beyond the expiration date	AMIR ZARABIAN SUPERVISORY PATENT EXAMINER						
of U.S Patent. No.				ISSUE FEE			
	TECHNOLOGY CENTER 2800		2000	Amount Due	Date Paid		
	(Primary E	xaminer)	(Date)	\$1780,00	1-42-03		
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ISSUE FEE IN FILE